DC-Link Capacitors for Grid Based Inverter Applications

Angelo Yializis Ph.D. Sigma Technologies Int'l <u>ayializis@sigmalabs.com</u>
Phase I Program Funded by The DOE Energy Storage Program Office of Electricity
Delivery and Energy Reliability Managed by Dr. Imre Gyuk, TPOC: Dr. Stanley Atcitty

THE CHALLENGE

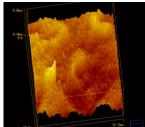
The DC-link capacitor is one of the largest, costliest and most failure-prone components in today's inverter systems

PHASE I PROJECT OBJECTIVES

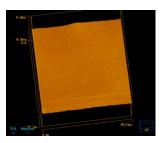
- Increase temperature capability
- Improve life and reliability
- Perform the above without changing the way capacitors are currently manufactured

DEVELOPMENT APPROACH

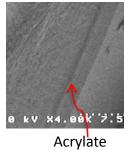
- Convert commercially available capacitor films
- Electron beam cross linked Acrylate polymers
- Pinhole free, breakdown strength 1000V/um
- Temperature 260°C, Dielectric constant k=3.0 to 6.2



AFM of Base Film



Acrylate Coated Film



PHASE I DEMONSTRATED

- 80% to 140% increase in I²R thermal load capacity before failure
- 0-10% higher breakdown strength
- 10-20% Higher dielectric constant
- 10X higher resistance to corona degradation
- 70% lower ESR
- >10X life at dV/dt of 1000V/µs

PHASE II DEVELOPMENT

- Scale up to a roll to roll pilot line
- Optimize dielectric stack
- Produce 800μF/1000V capacitors
- Demonstrate performance using specific application based capacitor tests



Sigma Technologies Intl'